

Structural characterization of partially relaxed hybrid radial (Pb,Sn)Te/WZ-GaAs nanowires as promising candidates for topological insulator nano-devices

<u>M. Wojcik¹, S. Dad¹, W. Zajkowska-Pietrzak¹, P. Dziawa¹, S. Kret¹, W. Pacuski², J. Sadowski^{1,2,3}</u>

¹ Institute of Physics, Polish Academy of Sciences, Aleja Lotnikow 32/46, PL-02-668 Warsaw
² Faculty of Physics, University of Warsaw, Pasteura 5, 02093 Warsaw, Poland
³ Ensemble3 Centre of Excellence, Wolczynska, 01-919 Warsaw, Poland

Introduction Topological crystalline insulators (TCIs) belong to extensively investigated research area of topological quantum matter. The interest in TCIs stems from the presence of topologically protected Dirac states on high symmetry surfaces of TCI crystals. (Pb,Sn)Te solid solution is one representative of TCIs, with

a

critical Sn content. Since topologically protected states in TCIs are surface properties we have studied this material in quasi one dimensional nanowire (NW) geometry providing high surface-to-volume ratio. To further enhance the surface-related properties we have chosen to investigate coreshell NW heterostructures with wurtzite (WZ) GaAs cores and (Pb,Sn)Te shells [1] instead of uniform NWs [2,3].

topological phase occurring above some

Experimental

Core-shell NW heterostructures have been grown by molecular beam epitaxy using two distinct MBE systems dedicated to III-V, and IV-VI semiconductors. The interface structure of WZ-GaAs/(Pb,Sn)Te NWs is investigated using wide range of characterization techniques, such as high resolution transmission electron microscopy (HR-TEM), scanning transmission electron microscopy (STEM), geometric phase analysis (GPA) and energy dispersive x-ray spectroscopy (EDX).

Dislocation and moiré fringes distances as well as lattice mismatch values were calculated using simple equations:



b)

mismatch between the core and the shell materials. Measured distances of moiré fringes match calculated spacings of misfit dislocations, which are observed to be higher than the calculated values, suggesting the presence of a residual strain within the structures.

Fig. 1 SEM image (a) and STEM image of core-shell interface (b) of WZ-GaAs/PbSnxTe x=0.53 nanowires, HR-TEM images of core-shell interfaces of WZ-GaAs/PbSnxTe x=0.63 (c) and WZ-GaAs/PbTe (d) nanowires. Red circles marks positions of dislocations.

$$f = (a_{sub} - a_{lay})/a_{lay}$$
$$d_{dist} = a_{lay} * \left(\frac{a_{lay}}{a_{sub} - a_{lay}}\right)$$
$$d_{moire} = \frac{a_{sub} * a_{lay}}{a_{sub} - a_{lay}}$$

Conclusions

We have shown that (Pb,Sn)Te can be successfully grown as continuous full or halfshells on the sidewalls of GaAs NWs. This provides opportunity for investigation of topological surfaces of TCIs in the tubular geometry.

Measured moiré distances match measured dislocation distances in measured samples. This provide opportunity for complementary method of strain analysis in investigated materials.



Fig. 2 HR-TEM images of WZ-GaAs/PbSn_xTe x=0.53 (a,b) and WZ-GaAs/PbSn_xTe x=0.63 (c) nanowires with marked averaged moiré fringes distances.

Sample	a _{shell} (nm)	a _{Core} (nm)	dislocation distance (calculated, nm)	dislocation distance (measured, nm)	moiré distance (calculated, nm)	moiré distance (measured, nm)	lattice mismatch (calculated)
PbSn0.53Te	0.333	0.345	8.99	17.13	9.57	17.73	0.036
PbSn0.63Te	0.321	0.336	6.81	9.34	7.19	7.55	0.047
PbTe	0.318	0.342	4.23	4.99			0.075

References and acknowledgements

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Tab. 1 calculated and measured dislocation and moiré fringes distances and calculated lattice mismatch for presented samples.

